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Solid-State Circuits, IEEE Journal of , Volume: 34 , Issue: 11 , Nov. 1999

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[Abstract] [PDF Full-Text (468 KB)] IEEE JNL

**3 Exploring SW performance using SoC transaction-level modeling**

Moussa, I.; Grellier, T.; Nguyen, G.;

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*Friendly, D.H.; Patel, S.J.; Patt, Y.N.;*

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*Martinian, E.; Sundberg, C.E.W.;*

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*Demosthenous, A.; Taylor, J.;*

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*Xiaoyan Sun; Shipeng Li; Feng Wu; Jacky Shen; Wen Gao;*

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*Kamali, B.; Morris, P.;*

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